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Date:

February 13, 2008

To:

United States Patent and Trademark Office

Examiner: Chen, Kin Chan; Art Unit: 1792

Fax:

(571) 273-8300

Re:

Application Serial No.: 10/705,347

Filing Date: 11/8/2003; First-Named Inventor: Labelle

Attorney Docket No.: 0180151

From:

Farjami & Farjami LLP

Number of pages including the cover sheet: 4

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1 2/00	For: Method for Integrating a High-K Gate Dielectric in a				
on $\frac{1}{2}$	Transistor Fabrication Process				
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Typed or printed name Carter Ellis			•		
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registration number 30,133			phone number		
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